

**Rama University** Uttar Pradesh, **Kanpur**  
**Faculty of Engineering & Technology**






Ref: RU/FET/ECE/BOS/2019/001

Dated: 29/04/2019

Faculty of Engineering & Technology  
Department of Electronics & Communication Engineering

Minutes of Meeting  
Boards of Studies

A meeting of Boards of Studies of Electronics & Communication Engineering, FET was held on 27/04/2019 (Saturday) at 2:30 PM. in conference room of FET. The following members were present:

- |                           |          |  |
|---------------------------|----------|--|
| 1. Mr. Samir Kumar Mishra | - Member |  |
| 3. Mr. Deleep Kumar       | - Member |   |
| 3. Mr. Raghvendra Singh   | - Member |   |

The following members agreed to review the minutes in Delhi.

- |                       |                   |  |
|-----------------------|-------------------|--|
| 1. Prof. Shyam Akashe | - External Member |  |
| 2. Mr. Raj Johri      | - External Member |  |

**Agenda:**

**1. Action Taken Report (ATR) on Minutes of Previous Meeting.**

The BOS committee confirmed the minutes of the BOS meeting held on 28/04/2018.

**2. To review Result analysis.**

The Board reviewed the result analysis and found it to be acceptable but need more improvements.

**2. To consider the revision of syllabi of PG program.**

To consider the feedback of faculty members and Stack holder BOS decided that the syllabi should be revise. The updated syllabi will be considered thereof will be placed before the next BOS. A committee consisting of following member has been constituted for the compilation of the revised syllabus.

1. Mr. Samir Kumar Mishra
2. Mr. Deepak Patel
3. Prof. Shyam Akashe

**3. Recommendation on New courses/Short term training**

S. No.	Item No.	Feedback from Faculty/Student	Recommendation /Action Taken
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1	To consider and approved the short term course on advanced in MEMS	Advanced in MEMS course is designed to bring students up to speed in their knowledge of field used to create tiny integrated devices or systems that combine mechanical and electrical components. They are fabricated using integrated circuit (IC) batch processing techniques and can range in size from a few micrometers to millimetres	The BOS consider and approved.
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The meeting concluded with a vote of thanks to the chair.

Date of the Next Meeting: to be decided and conveyed later

(Chairman)

- CC:  
1. Dean  
2. Registrar Office